

**Amendments to the Specification:**

Please replace paragraph [0035] of the specification with the following paragraph:

**[0035]** Next, the ACPI OS obtains the dummy address and uses it to access the throttle state (Block 325). Then, the ACPI OS accesses the throttling state as if it were accessing the actual ACPI structures such as the FADT, the power registers, etc. (Block 330). Next, the chipset generates [[and]] an I/O trap SMI to the SMI handler (Block 335). The SMI handler then executes the I/O trap for accessing the throttling state (Block 340).

Please replace paragraph [0037] of the specification with the following paragraph:

**[0037]** Figure 4 is a flowchart illustrating a process 400 to emulate throttling using a timer according to one embodiment of the invention.